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October 12, 2005

Certificate
OCT 19 2005
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Title: METHOD AND APPARATUS FOR SUPPORTING A
SEMICONDUCTOR WAFER DURING PROCESSING
Letters Patent No. 6,805,749
Issued: October 19, 2004
Our Reference: ASMINT.002C3

Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP

Adeel S. Akhtar
Registration No. 41,394
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Enclosures

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101105

OCT 21 2005

Orange County
949-760-0404

San Diego
619-235-8550

Los Angeles
310-551-3450

Riverside
951-781-9231

San Luis Obispo
805-547-5580

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,805,749 B2

DATED : October 19, 2004

INVENTOR(S): Ernst Hendrik August Granneman, Frank Huussen

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

First Page, Col. 1 (Inventors), change "Hunssen" to - -Huussen--.
Column 7, line 7, in Claim 1, after "surface," insert - -and- -.
Column 7, line 13, in Claim 1, after "surface," insert - -and- -.

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,805,749 B2

1984611
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FORM PTO 1050

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